

WHAT IS CLAIMED IS:

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All*
1. A projection type image display device comprising:
a plurality of image display elements for performing
modulation of light rays in accordance with an image signal;
a color synthesizing optical element, having at least a
dichroic membrane, for synthesizing light rays having
mutually different wavelength ranges that have been
modulated by said plurality of image display elements;
a lens group having positive refractive power as a
whole and disposed between the image display elements and
the color synthesizing optical element;
projection optical system for projecting an image
synthesized by the color synthesizing optical element;
storage circuit for storing data used to correct
brightness irregularity of an image projected by the
projection optical system; and
brightness irregularity correcting circuit for
correcting brightness irregularity of an image projected by
the projection optical system on the basis of the data
stored in the storage circuit.
 2. The projection type image display device according to
Claim 1, wherein the dichroic membrane is a gradient
membrane in which thickness or a refractive index varies in
a specific direction, and the brightness irregularity
correcting circuit corrects an image signal for each image

display element in directions other than the specific direction or in each direction including the specific direction.

3. The projection type image display device according to Claim 1, wherein the storage circuit stores data used to correct the brightness irregularity on a projection surface onto which the image has been projected, the brightness irregularity caused by the fact that non-parallel rays of light subjected to condensing by the lens group enter the dichroic membrane.

4. The projection type image display device according to any one of Claims 1, 2 and 3, wherein the brightness irregularity correcting circuit corrects an image signal in each pixel of the image display elements.

5. The projection type image display device according to any one of Claims 1, 2 and 3, wherein the brightness irregularity correcting circuit corrects an image signal in each pixel area of the image display elements.

ADD
All